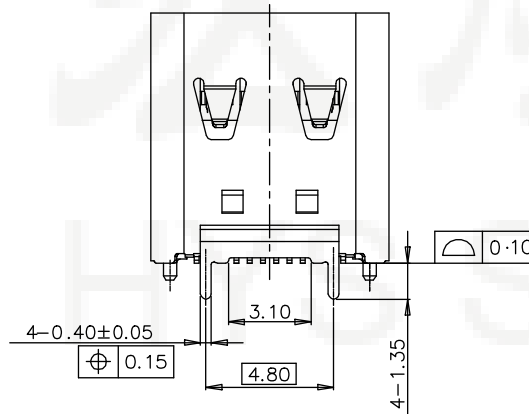
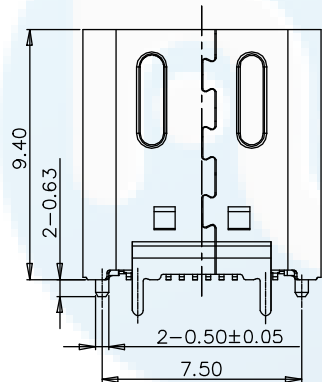
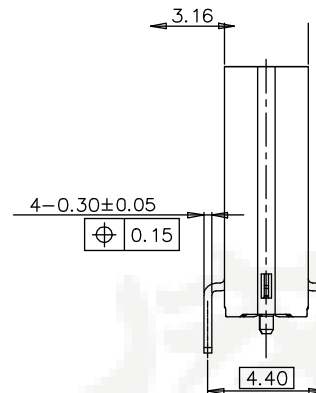
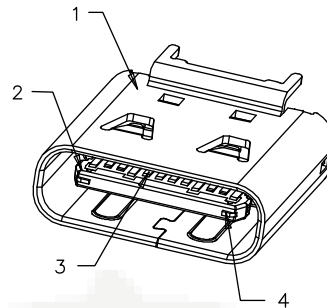
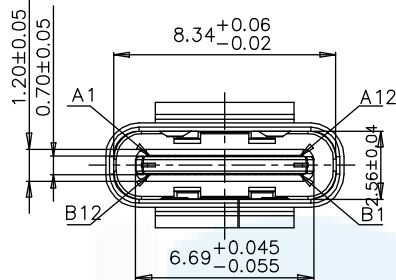


ROHS
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NOTES:

1. Electrical characteristics:
1-1. Contact current rating:
A current of 5.0A shall be applied collectively to VBUS pins and 1.25A

applied to the VCONN pin with the return path through the corresponding GND pins.

A MIN 0.25A shall be applied individually to all other contacts.

1-2. Contact resistance:
40 Milliohms (MAX) initial for VBUS, GND and all other contacts.

Maximum change (delta) of +10 Milliohms after environmental stresses.
1-3. Dielectric withstanding voltage : 100 V AC MIN (RMS).

1-4. Insulation resistance: 100 MEGOHMS MIN.

2. Mechanical characteristics:

2-1. Connector insertion force: 5~20N at a maximum rate of 12.5mm per minute.

2-2. Connector extraction force: 8~20N up to 1000 mating cycles, and 6~20N

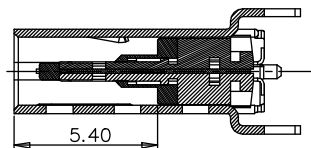
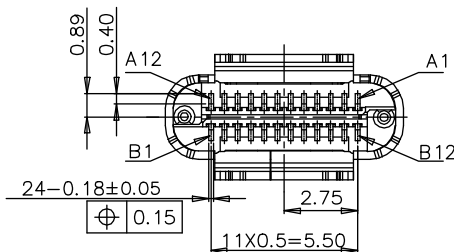
after the specified insertion/extraction or durability cycles at a maximum rate of 12.5mm per minute.

2-3. Durability: 10000 cycles minimum.

2-4. Operating temperature: -55°C TO +85°C

3. The connector is Rohs compliant and halogen free compliant.

PIN ASSIGNMENT			
Pin No.	Signal Name	Pin No.	Signal Name
A1	GND	B12	GND
A2	TX1+	B11	RX1+
A3	TX1-	B10	RX1-
A4	VBUS	B9	VBUS
A5	CC	B8	SBU2
A6	D+	B7	D-
A7	D-	B6	D+
A8	SBU1	B5	VCONN
A9	VBUS	B4	VBUS
A10	RX2-	B3	TX2-
A11	RX2+	B2	TX2+
A12	GND	B1	GND



Section A-A

Item	Description	QTY.	Material	Finish
4	Shield plate	1	Stainless stell	Clean
3	Contact	24	Copper Alloy	Ni underplating overall, Au plating at contact area, Sn plating at solder area.
2	Housing	1	Thermo Plastic, UL94V-0	Molded Color: Black
1	Shell	1	Stainless stell	Ni plating overall

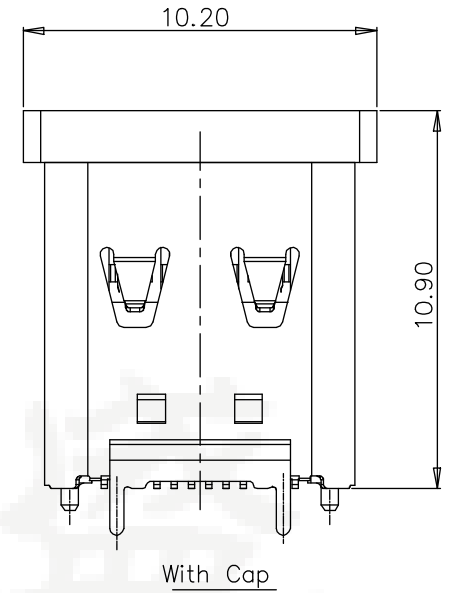
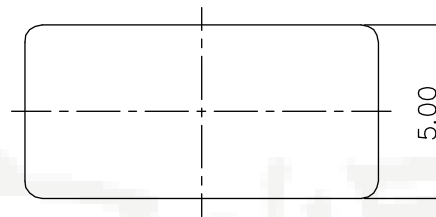
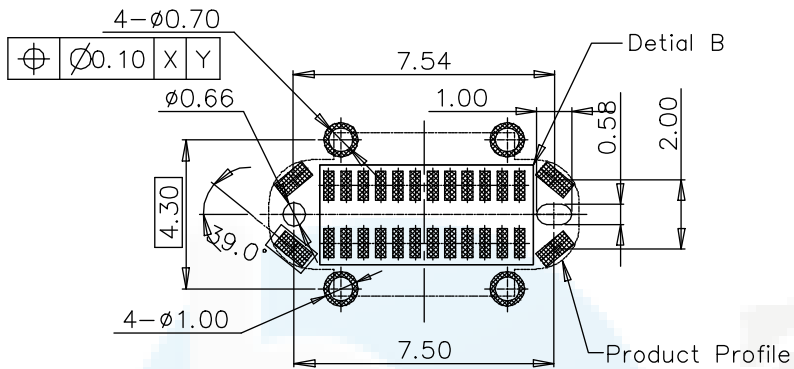
DESIGN	Li Jinwei	SCALL	
DRAWING	Wu Qifa	UNIT	mm
CHECK	Zheng Jinxian	SIZE	A4: 210 * 297
APPROVAL	Liu Xiaohong	DATE	2020-06-10

TOLERANCE	
X.X ±0.30	X.°±2°
.XX ±0.20	.X° ±1°
.XXX ±0.10	.XX°±0.5°

DRAWG NO.	TYPE-C-LDS24PZ940
TITLE:	24P立式贴片四脚插板SMT 9.4H

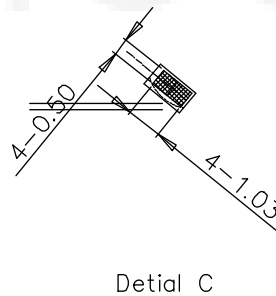
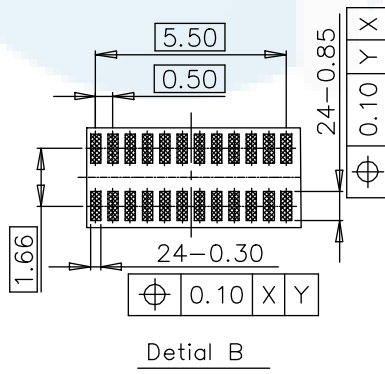
东莞市宏煜盛实业有限公司
Dongguan hongyusheng Industry Co., Ltd
EL: 0769-81230179 <https://www.hystypec.com>

ROHS
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RECOMMENDED P.C.B LAYOUT (COMPONENT SIDE)

PCB THICKNESS: 1.20±0.05mm
DEFAULT TOLERANCE: ±0.05



PIN ASSIGNMENT

Pin No.	Signal Name	Pin No.	Signal Name
A1	GND	B12	GND
A2	TX1+	B11	RX1+
A3	TX1-	B10	RX1-
A4	VBUS	B9	VBUS
A5	CC	B8	SBU2
A6	D+	B7	D-
A7	D-	B6	D+
A8	SBU1	B5	VCONN
A9	VBUS	B4	VBUS
A10	RX2-	B3	TX2-
A11	RX2+	B2	TX2+
A12	GND	B1	GND

DESIGN	Li Jinwei	SCALL	
DRAWING	Wu Qifa	UNIT	mm
CHECK	Zheng Jinxian	SIZE	A4: 210 * 297
APPROVAL	Liu Xiaohong	DATE	2020-06-10

TOLERANCE	
X.X ±0.30	X.°±2°
.XX ±0.20	.X°±1°
.XXX ±0.10	.XX°±0.5°

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