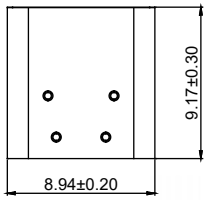
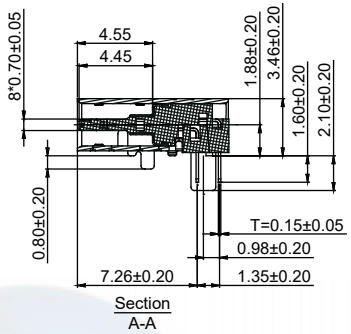
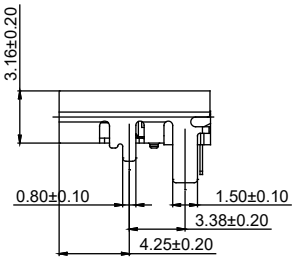
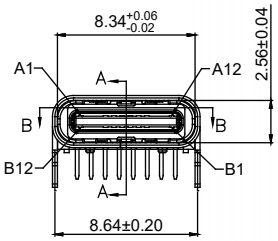
ROHS
Compatible

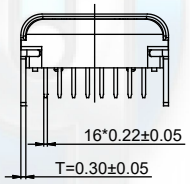


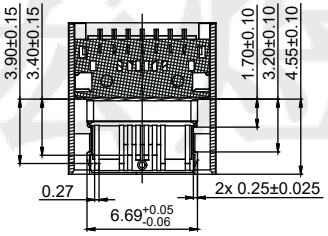


Section
A-A

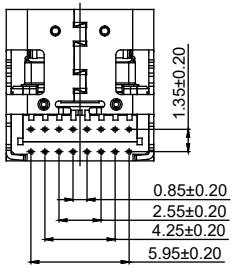


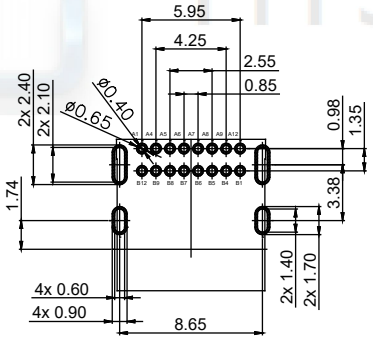






Section
B-B





RECOMMENDED PCB LAYOUT(TOP)
TOLERANCES:±0.05MM

Specifcations:
1.0 Electrical performance:
1.1 Contact resistance: 40m? max for initial. 10m? change after test. measure at 20mv, 100mA.
1.2 Dielectric withstanding voltage:AC 100V(rms).
1.3 Insulation resistance:100M? min.
1.4 Contact current rating: 3A for Vbus pin;4.25A for Vconn and GND pin;0.25A for all the other pin.
1.5 Voltage: 20 Vac (rms).
2.0 mechanical performance:
2.1 Mating force:5~20N
2.2 Unmating force:8~20N Initial test, 6~20N After test
2.3 Durability:10,000 cycles.
3.0 Environmental performance:
3.1 Temperature range:
3.2 Operating temperature:-40°to+85°.
3.3 Storage temperature:-20°to+60°.
4.0 Material:
4.1 Housing:LCP E130I Black,UL94-V0.
4.2 Contact:C7025,T=0.15mm
4.3 Mid-plate: SUS301,T=0.15mm
4.4 Shell:SUS304 T=0.30mm
4.5 Shell-EMI:SUS316 T=0.1
5.0 Plating specification:
5.1 Contact:Au(see P/N) plated on contact area Matte Tin plated 50u"min on solder area;Nickel 50u"min underplating over all.
5.2 Shell: 50u"min nickel plating.
5.3 Shell-EMI:None plating.

U5	506-	16P	x	9.17L
↓	↓	↓	↓	↓
名称	流水号	PIN位	N;母座	L;长度

A1	GND	B12	GND
A2	N/A	B11	N/A
A3	N/A	B10	N/A
A4	VBUS	B9	VBUS
A5	CC1	B8	SBU2
A6	DP1	B7	DN2
A7	DN1	B6	DP2
A8	SBU1	B5	CC2
A9	VBUS	B4	VBUS
A10	N/A	B3	N/A
A11	N/A	B2	N/A
A12	GND	B1	GND
PIN	SIGNAL NAME	PIN	SIGNAL NAME

DESIGN	Li Jinwei	SCALL	
DRAWING	Wu Qifa	UNIT	mm
CHECK	Zheng Jinxian	SIZE	A4: 210 * 297
APPROVAL	Liu Xiaohong	DATE	2020-06-10

TOLERANCE
X.X ±0.30 X.°±2°
.XX ±0.20 .X° ±1°
.XXX ±0.10 .XX°±0.5°

DRAWG NO.
U5506-16PN9.17L
TITLE:
16P 板上四脚插板DIP 9.17L

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